

# WEST Search History

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*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

☐ L21 112 and test\$ adj1 signal 50

☐ L20 112 and test\$ adj2 signal 69

*DB=JPAB; PLUR=YES; OP=ADJ*

☐ L19 L17 and (SRAM or test\$ or burn\$ or simultaneous\$ or memory) 20

☐ L18 L17 and memory 17

☐ L17 L16 and select\$ 34

☐ L16 write driver\$ 115

*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

☐ L15 L13 and burn\$ 26

☐ L14 L13 and test\$ 57

☐ L13 L12 and SRAM 124

☐ L12 L11 and select\$ adj3 column\$ 240

☐ L11 L10 and input 348

☐ L10 L9 and simultaneous\$ 349

☐ L9 L8 and (NAND\$ or AND\$) adj1 gate\$ 490

☐ L8 L7 and data 1053

☐ L7 15 and (block\$ or array\$) adj3 select\$ 1055

☐ L6 15 and (block\$ or array\$) adj3 (enabl\$ or activat\$ or select\$) 1209

☐ L5 L3 and write adj2 driver\$ 2852

☐ L4 L3 and write adj2 driver 2852

☐ L3 L1 and (array\$ or block\$) 533636

☐ L2 L1 and (array\$ or block\$) 533636

☐ L1 memory 666510

VIET Q. NGUYEN  
PRIMARY EXAMINER

END OF SEARCH HISTORY